

# Ingenieur-Büro Kirchen

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## SSI-Encoder Simulator

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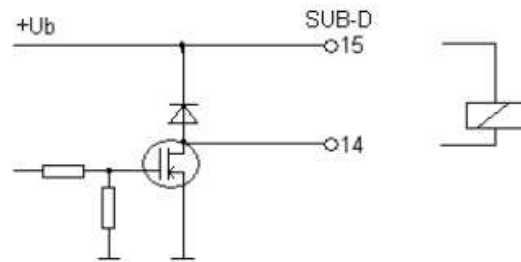


Infonummer: 01027-01

### Header – Pin function

Pin Nr.	Description
1	GND
2	RS422 OUT Dat +
3	RS422 IN Clk +
4	res.
5	res.
6	res.
7	res.
8	+U <sub>B</sub> (12VDC to 30VDC)
9	res.
10	RS422 OUT Dat -
11	RS422 IN Clk -
12	res.
13	res.
14	Digital Output o.D. driver
15	Digital Output o.D. power

### Digital Output



### Electrical characteristics

Parameter	Symbol	Min	Typ	Max	Unit
DC Supply Voltage	U <sub>B</sub>	12	24	30	[V]
DC Supply Current (Standby)	I <sub>B</sub>		15		[mA]
Number of databits	N	8		24 / 32	
External Clock Frequency	f <sub>CLK</sub>	40		500	[kHz]
CLK to DAT out (after CLK L→H trigger)	t <sub>ed</sub>	900		1100	[ns]
CLK – H-time	t <sub>H</sub>	1000		12500	[ns]
CLK – L-time	t <sub>L</sub>	1000		12500	[ns]
CLK Termination (CLK+ to CLK-)	R <sub>CLK</sub>		200		[Ω]
DAT Hold Time (at end of transmission)	t <sub>m</sub>		10		[μs]
CLK Idle Time	t <sub>I</sub>	25			[μs]
Operating Temperature	T <sub>A</sub>	0		50	[°C]
IP	IP	00		20	

### SSI transfer function

